

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. PF020158		SERIAL NO.	
INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 (Use several sheets if necessary)				APPLICANTS Centen et al.			
				FILING DATE Herewith		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINE INITIAL		DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)							
	AR	Patent Abstract of Japan; Vol. 014, No. 028, Jan. 19, 1990 & JP 01 264481 A, October 20, 1989					
	AS	Patent Abstract of Japan; Vol. 018, No. 455, Aug. 24, 1994 & JP 06 141328A, May 20, 1994					
	AT	Yamada T et al.: "Driving Voltage Reduction in a Two-Phase CCD by Suppression of Potential Pockets in Inter-Electrode Gaps"; Vol. 44 No. 10, October 1, 1997 pages 1580-1587					
EXAMINER				DATE CONSIDERED			
SUBMITTED BY:				REG.NO.:		DATE:	
Patricia A. Verlangieri				42,201		11/24/03	